

BEC 2012		WEDNESDAY 3 Oct.		Tallinn University of Technology, Building 7, Room VII 226	
		REGISTRATION:	Wednesday 3 Oct. 9.00-15.00 Thursday 4 Oct. 8.00-17.00 Friday 5 Oct. 9.00-12.00	➤ Tallinn University of Technology, Building 7 ➤ Laulasmaa Resort and SPA hotel ➤ Laulasmaa Resort and SPA hotel	
13:00 - 15:00 OPENING PLENARY SESSION Chair: Prof. Toomas Rang					
13:00 The conference opening, T. Rang and A. Keevallik					
13.15 INVITED TALK 1		SiGe Technology for mm-Wave applications Saurabh Sinha, Daniel Foty, University of Pretoria, South Africa			
13.45 INVITED TALK 2		THz Spectroscopic Studies of Biomolecules Anthony J. Vickers, D. Crompton, School of Computer Science and Electronic Engineering, University of Essex, UK			
14.15 INVITED TALK 3		Computation in living cells: implementation and control strategies Ion Petre, Åbo Academi University, Finland			
14.45 Closing the open plenary					
15.30 - The bus to Laulasmaa					
18.00 - 20:00 The welcome cocteil of the chairman (at Laulasmaa Resort and SPA hotel)					
		THURSDAY 4 Oct.		Laulasmaa Resort and SPA hotel	
room A		room B		room C	
10:00-11:00	1. Semiconductor Electronics: Technologies, Devices, and Simulation SESSION 1-1: Simulation Chair: Prof. Enn Velmre	3. Test, Verification, and Validation SESSION 3-1: Fault Models and Testing Chair: Dr. Jaan Raik		8. Power Electronics SESSION 8-1: Power Electronic for Distributed Energy Systems Chair: Dr. Indrek Roasto	
11.00 - 11.30 COFFEE					
11:30-12:30	1. Semiconductor Electronics: Technologies, Devices, and Simulation SESSION 1-2: Experiments and Optimization Chair: Dr. Oleg Korolkov	3. Test, Verification, and Validation SESSION 3-2: System Verification and Reliability Chair: Prof. Raimund Ubar		8. Power Electronics SESSION 8-2: Power Electronics for Lighting Applications Chair: Prof. Ilya Galkin	
12:30-13:30	4. Embedded Systems SESSION 4-1: Embedded Systems Validation Chair: Prof. Thomas Hollstein	3. Test, Verification, and Validation SESSION 3-3: Analog and Mixed Signal Testing Chair: Prof. Peeter Ellervee		8. Power Electronics SESSION 8-3: Power Electronics and Electrical Engineering Chair: Dr. João Martins	
13.30 - 14.30 CONFERENCE LUNCH					
14:30-15:30	2. Integrated Electronics Systems SESSION 2-1: Circuit Design Chair: Prof. Peeter Ellervee	6. Instrumentation and Communication SESSION 6-1: Data Acquisition, Communication and Control I Chair: Dr. Alar Kuusik		8. Power Electronics SESSION 8-4: Power Electronics Chair: Dr. Dmitri Vinnikov	
15.30 - 16.00 COFFEE					
16:00-17:00	10. System Identification SESSION 10-1: Instrumentation and Signals Chair: Dr. Toivo Paavle	6. Instrumentation and Communication SESSION 6-2: Data Acquisition, Communication and Control II Chair: Dr. Jaan Ojarand		9. Signal Processing SESSION 9-1: Bio-impedance and Acoustical Signal Processing Chair: Dr. Rain Ferenets	
17:00-19:00	Doctoral School presentations	Doctoral School presentations		Doctoral School presentations	
19.00 - 21:00 Conference Dinner					
		FRIDAY 5 Oct.		Laulasmaa Resort and SPA hotel	
room A		room B		room C	
10:00 - 11:00	10. System Identification SESSION 10-2: Modeling and Parameter Estimation Chair: Dr. Raul Land	6. Instrumentation and Communication SESSION 6-3: Radar solutions Chair: Dr. Toomas Parve		9. Signal Processing SESSION 9-2: Image and Other Signal Processing Chair: Dr. Olev Märtens <i>Coffee</i>	
11.00 - 11.30 COFFEE					
11:30-12:30	7. Biomedical Electronics SESSION 7-1: Physiological Signals and Sensors Chair: Prof. Kalju Meigas	6. Instrumentation and Communication SESSION 6-4: Vehicle Control and Positioning Chair: Dr. Rauno Gordon		Doctoral School presentations	
12:30-13:30	7. Biomedical Electronics SESSION 7-2: Monitoring and Modelling of Biological Objects Chair: Dr. Toivo Paavle	Special Session: Learning is Engineering Chair: Prof. Vello Kukk		Doctoral School presentations	
13.30 - 14.30 CONFERENCE LUNCH					
14:30 - 16:00 CLOSING PLENARY SESSION Chair: Prof. Toomas Rang					
14.30 INVITED TALK 4		The Future of "Moore's Law" – Does It Have One? Daniel Foty, Gilgamesh Associates, The United States of America			
15.00 INVITED TALK 5		Smart Homes and Smart Buildings João Martins, J. A. Oliveira-Lima, V. Delgado-Gomes, R. Lopes, D. Silva, S. Vieira, Nova de Lisboa-FCT-DEE and UNINOVA-CTS, Portugal, C. Lima, Universidade Federal do Oeste do Pará, Brazil			
15.30		Award of best paper and closing of the Conference Toomas Rang			
16.30 - The bus back to Tallinn (BUS STATION, AIRPORT, RAILWAY STATION, CITY CENTRE)					