

Sunday 6 Oct.

16.00-18.00 Registration

BEC2002

TIMETABLE

Monday 7 Oct.

8.00-17.00 Registration

9.30 **Plenary Session**Opening address Prof Toomas Rang
Greetings Rector Andres Keevallik

9.45 Invited talk 1

Venture Capital and Financing of Technology Projects, Olev Schults, *Estonia*

10.15 Invited talk 2

The European Union 6th Framework Programme for Research and Development, Javid Khan, *Belgium*

10.45 Invited talk 3

40 years of Electronics Department at Tallinn Technical University, Uljas Tamm, Enn Velvre, *Estonia*

11.15-11.30 Coffee

11.30 Invited talk 4

Integrated Circuit Design for Personal Communication Systems, Veikko Porra, Kari Halonen, *Finland*

12.00 Invited talk 5

Conceived Performance of Re-Configurable IC Architectures, Tuomas P. Valtonen, Jouni Isoaho, Hannu Tenhunen, *Sweden*

12.30 Invited talk 6

Processing Signals Digitally in a Much Wider Frequency Range, Ivars Bilinskis, *Latvia*

13.00-14.00 Lunch break

14.00-16.00 **7. Power Electronics**
(Chair: J. Järvi)P-005
P-012
P-024
P-044
P-069
P-071**5. Telecommunication and Optical Transmission**
(Chair: A.Ots)P-028
P-050
P-058
P-060
P-063**6. Biomedical Electronics**
(Chair: K. Meigas)P-036
P-053
P-070
P-081
P-111
P-10312.00-18.00
Tutorials on Analog and Digital Test**TUTORIAL 1: Design for Test of Systems on Chip: Analog Test**
Presenters: V. Stopjakova, V. Mosin, M. Blyzniuk

16.00-16.30 Coffee

16.30-17.40 **Posters:**
7. Power ElectronicsP-006
P-025**Posters:**
5. Telecommunication and Optical Transmission
P-003
P-013
P-061
P-062
P-078**Posters:**
6. Biomedical Electronics
P-038
P-049
P-104**TUTORIAL 1**

18.00-19.00 Welcome Reception

Tuesday 8 Oct.

8.00-17.00 Registration

9.00-10.20 **4. Test, Diagnostics, and Fault Tolerance**

Session 1. Fault tolerance and built-in test (Chair: R. Ubar)

P-011
P-064
P-007
P-092**2. Integrated Electronics and Chip Design**

Session 1. (Chair: V. Kukk)

P-030
P-031
P-032
P-033**Special Session: Digital Alias-Free Signal Processing**(Chair: I. Bilinskis)
room All-208P-095
P-100
P-094
P-096

10.40-11.00 Coffee

11.00-12.20 **4. Test, Diagnostics, and Fault Tolerance**

Session 2. Fault simulation and diagnosis (Chair: B. Magnhagen)

P-016
P-029
P-107
P-088**2. Integrated Electronics and Chip Design**

Session 2. (Chair: V. Männama)

P-051
P-067
P-068
P-072**Special Session: Digital Alias-Free Signal Processing**(Chair: I. Bilinskis)
room All-208P-097
P-098
P-099
P-101

12.40-14.00 Lunch break

14.00-15.20	<p>4. Test, Diagnostics, and Fault Tolerance</p> <p>Session 3. Test pattern generation (Chair: E. Gramatova)</p> <p>P-074 P-114 P-004 P-106</p>	<p>2. Integrated Electronics and Chip Design</p> <p>Session 3. (Chair: E. Kängsep)</p> <p>P-026 P-057 P-110</p>
-------------	--	--

15.40-16.00 Coffee

16.00-17.40	<p><u>Posters:</u></p> <p>4. Test, Diagnostics, and Fault Tolerance</p> <p>P-073 P-042 P-079 P-077 P-086</p>	<p><u>Posters:</u></p> <p>2. Integrated Electronics and Chip Design</p> <p>P-035 P-040 P-043 P-102 P-105</p>
-------------	---	---

18.30-20.00 Conference Party

Wednesday 9 Oct.

9.00-10.20	<p>1. Electronic Materials, Devices, and Simulation</p> <p>8. Micromechanical Systems</p> <p>Session 1. (Chair: A. Udal)</p> <p>P-083 P-034 P-001 P-048</p>	<p>3. Instrumentation and System Design</p> <p>Session 1. (Chair: M. Min)</p> <p>P-008 P-021 P-022 P-082</p>	<p>9.00-18.00</p> <p>Tutorials on Analog and Digital Test</p> <p><u>TUTORIAL 2</u>: Design for Test of Systems on Chip: Digital Test</p> <p>Presenters: J. Hlavicka, V. Drabek, O. Novak, Z. Pliva, Z. Kotasek, E. Gramatova</p>
------------	---	---	---

10.40-11.00 Coffee

11.00-12.40	<p>1. Electronic Materials, Devices, and Simulation</p> <p>8. Micromechanical Systems</p> <p>Session 2. (Chair: E. Velmre)</p> <p>P-080 P-087 P-090 P-065</p>	<p>3. Instrumentation and System Design</p> <p>Session 2. (Chair: T.P. Dobrowiecki)</p> <p>P-084 P-093 P-023 P-027 P-037</p>	<p><u>TUTORIAL 2</u></p>
-------------	---	---	--------------------------

12.40-14.00 Lunch break

14.00-15.40	<p><u>Posters:</u></p> <p>1. Electronic Materials, Devices, and Simulation</p> <p>8. Micromechanical Systems</p> <p>P-041 P-059 P-066 P-089 P-113</p>	<p>3. Instrumentation and System Design</p> <p>Session 3. (Chair: P. Misans)</p> <p>P-010 P-045 P-054 P-075 P-091</p>	<p><u>TUTORIAL 2</u></p>
-------------	---	--	--------------------------

15.40-16.00 Coffee

16.00-17.40	<p><u>Posters:</u></p> <p>3. Instrumentation and System Design</p> <p>P-014 P-015 P-046 P-047 P-055 P-056 P-076 P-085 P-108 P-112 P-115</p>	<p><u>TUTORIAL 2</u></p>
-------------	--	--------------------------

18.00 Closing of the Conference