

# Scientific Programme

Monday 9 Oct.

10.00-10.20	Opening address Plenary Session
10.20-11.00	<b>Invited talk 1:</b> <b>"Future Research, Application and Education Perspectives of Complex Systems-on-Chip (SoC)"</b> Manfred Glesner, Jürgen Becker, Thilo Pionteck, <i>Darmstadt University of Technology, Germany</i>
11.00-11.40	<b>Invited talk 2:</b> <b>"Elcoteq and technology providers in today's networking economy"</b> Jorma Vanhanen, <i>Elcoteq Network Corporation, Finland</i>
12.40-14.00	Lunch break
14.00-15.20	1. Electronic Materials and Package Technologies 2. Semiconductor Devices and Simulation Session 1 Chair: Andres Udal, <i>Tallinn Technical University, Estonia</i>
14.00-14.20	<b>Technomics of Design and Manufacturing for Future Si Nanoelectronics</b> A. N. Bubennikov, A. A. Bubennikov, <i>Moscow Institute of Physics and Technology, Russia</i>
14.20-14.40	<b>Improvements in the presentation of frequency dependence of complex permeability for polycrystalline ferrites</b> J. Jankovskis, <i>Riga Technical University, Latvia</i>
14.40-15.00	<b>A Brief Survey of Exciton Concentration Models for Si</b> E. Velmre, A. Udal, <i>Tallinn Technical University, Estonia</i>
15.00-15.20	<b>Comparative Characteristics of Diffusion Welded Al Contacts to 6H- and 4H-SiC substrates</b> O. Korolkov, T. Rang, <i>Tallinn Technical University, Estonia</i>
15.40-16.00	Coffee
16.00-17.40	3. Integrated Electronics and Chip Design Session 1 Chair: Vello Kukk, <i>Tallinn Technical University, Estonia</i>
16.00-16.20	<b>A Low Power Approach for Busses Using Adiabatic Charging</b> B. Voss, M. Glesner, <i>Darmstadt University of Technology, Germany</i>
16.20-16.40	<b>Optimization of adiabatic clock generators</b> J. Kosonen, <i>Helsinki University of Technology, Finland</i> , M. Åberg, J. Marjonen, <i>VTT Microelectronics, Finland</i> , P. Kuivalainen, <i>Helsinki Univ. of Technology, VTT Microelectronics</i>

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12.40-14.00	Lunch break
14.00-15.20	4. Instrumentation and System Design Session 1 Chair: Mart Min, <i>Tallinn Technical University, Estonia</i>
14.00-14.20	<b>Design of two-rate stochastic multivariable discrete-time control systems</b> I. Astrov, E. Rüstern, <i>Tallinn Technical University, Estonia</i>
14.20-14.40	<b>Robot de Neuro – an Intelligent Mobile Robot for Navigation in Dynamic Environments</b> M. Kruusmaa, O. Bengtsson, A.-J. Baerfeldt, <i>Halmstad University, Sweden</i>
14.40-15.00	<b>Knowledge Fusion for Financial Advisory Systems</b> T. Dobrowiecki, G. Strausz, T. Mészáros, <i>Budapest University of Technology and Economics, Hungary</i>
15.00-15.20	<b>Genetic Algorithms in Transparent Fuzzy Modeling</b> A. Riid, P. Jartsev, E. Rüstern, <i>Tallinn Technical University, Estonia</i>
15.40-16.00	Coffee
16.00-17.20	4. Instrumentation and System Design Session 2 Chair: Tadeusz Dobrowiecki, <i>Budapest University of Technology and Economics, Hungary</i>
16.00-16.20	<b>Using XML in Intelligent Product Manuals</b> T. Mészáros, Z. Barczikay, <i>Budapest University of Technology and Economics, Hungary</i>
16.20-16.40	<b>Predictive Integration of Polynomial Signals</b> O. Vainio, T. Saramäki, <i>Tampere University of Technology, Finland</i>

- 16.40-17.00 **Test signal generation and estimation in a direct digital filter tuning system**  
T. Salo, S. Lindfors, K. Halonen, *Helsinki Univ. of Technology, Finland*
- 17.00-17.20 **Low-power Matched Filter for a Oversampling WCDMA Receiver**  
L. Koskinen, M. Kosunen, S. Lindfors, K. Halonen, *Helsinki University of Technology, Finland*
- 17.20-17.40 **A 32 x 32 CMOS Photogate Active Pixel Sensor (APS) Matrix**  
A. Leppänen, *Technical Research Centre of Finland, Finland*

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## Tuesday 10 Oct.

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- 9.00-9.40 **Invited talk 3:  
"Some challenges in implementing future wireless terminals"**  
Petteri Alinikula, *Nokia, Finland*
- 10.00-10.40 **5. Test, Diagnostics and Fault Tolerance Session 1**  
Chair: Raimund Ubar, *Tallinn Technical Univ., Estonia*
- 10.00-10.20 **Fault Oriented Test Pattern Generation for Sequential Circuits Using Genetic Algorithms**  
E. Ivask, J. Raik, R. Ubar, *Tallinn Technical University, Estonia*
- 10.20-10.40 **Greedy Alternative for the Static Compaction of Sequential Circuit Test Sequences**  
J. Raik, *Tallinn Technical University, Estonia*
- 10.40-11.00 **Coffee**
- 11.00-12.00 **5. Test, Diagnostics and Fault Tolerance Session 2**  
Chair: Elena Gramatova, *Inst. of Informatics, Slovak Republic*
- 11.00-11.20 **Finite State Machine Synthesis for Low Power Using Input-Disabling Precomputation Architectures**  
A. Sudnitsõn, *Tallinn Technical University, Estonia*
- 11.20-11.40 **FSM Decomposition Using Shift Registers**  
M. Kruus, H. Lensen, *Tallinn Technical University, Estonia*
- 11.40-12.00 **Hierarchical Fault Simulation for Finite State Machines**  
M. Brik, J. Raik, R. Ubar, *Tallinn Technical University, Estonia*
- 12.40-14.00 **Lunch break**

- 16.40-17.00 **A Modified Frequency Error Estimator for the Block Adaptive Fourier Analyzer**  
A. Ronk, *Tallinn Technical University, Estonia*
- 17.00-17.20 **FPGA-Based Rapid Prototyping: An Overview**  
H. Krupnova, G. Saucier, *Institut National Polytechnique de Grenoble, France*

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## Tuesday 10 Oct.

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- 10.00-10.40 **6. Telecommunication and Optical Transmission Session 1**  
Chair: Aimur Raja, *Tallinn Technical University, Estonia*
- 10.00-10.20 **Network Bandwidth Allocation Optimization for the Transmission of MPEG-4 Data Streams**  
U. Mayer, M. Glesner, *Darmstadt University of Technology, Germany*
- 10.20-10.40 **Applying of the NMT-450 technology for approach of the public switching telephone usage**  
J. Minkevičienė, *AB "Lietuvos Telekomas", Lithuania*
- 10.40-11.00 **Coffee**
- 11.00-12.00 **6. Telecommunication and Optical Transmission Session 2**  
Chair: Avo Ots, *Tallinn Technical University, Estonia*
- 11.00-11.20 **A spectrum-based technique to analyze a pair of digitized signals**  
E. Lossmann, A. Meister, A. Raja, *Tallinn Technical Univ., Estonia*,  
V. Tuulik, *Estonian Institute of Experimental and Clinical Medicine, Estonia*
- 11.20-11.40 **Integrated CNN Chaos Source**  
T. Huhtanen, V. Porra, *Helsinki University of Technology, Finland*
- 11.40-12.00 **The Evaluation of Radio Frequency Spectrum Utilisation Efficiency in Cellular Mobile Radio Systems**  
K. Bogens, *Latvia Telecommunication State Inspection, Latvia*
- 12.00-12.20 **Demodulation and Bit-synchronization Unit for FM-DCSK Chaos Radio**  
V. Porra, L. Azzinnari, K. Krol, E. Korpela, M. Talonen, *Helsinki University of Technology, Finland*
- 12.40-14.00 **Lunch break**

14.00-15.40	5. Test, Diagnostics and Fault Tolerance Session 3 Chair: Jan Hlavicka, <i>Czech Technical University, Czech Republic</i> , Gordon Russell, <i>University of Newcastle upon Tyne, UK</i>
14.00-14.20	<b>Analysis of the Application of Modified Berger Codes to Self-Checking VLSI Circuits</b> G. Russell, A. H. Maamar, <i>University of Newcastle upon Tyne, UK</i>
14.20-14.40	<b>M-Sequence Testing of Embedded Switch Capacitor Filters</b> G. Russell, M. Robson, <i>University of Newcastle upon Tyne, UK</i>
14.40-15.00	<b>Dependability Model of a Distributed Authentication System</b> V. Vais, St. Racek, <i>University of West Bohemia, Czech Republic</i> , J. Hlavicka, <i>Czech Technical University, Czech Republic</i>
15.00-15.20	<b>An Automated Verification Process Based on Scan Techniques</b> G. Alves, <i>ISEP, Portugal</i> , M. S. Lubaszewski, M. R. Krug, <i>UFRGS, Brazil</i> , J. M. M. Ferreira, <i>FEUP, Portugal</i>
15.20-15.40	<b>A symbolic closed form approach for the fault diagnosis of analog linear circuits</b> G. Fedi, S. Manetti, M. C. Piccirilli, <i>University of Florence, Italy</i>
15.40-16.00	Coffee
16.00-17.40	3. Integrated Electronics and Chip Design Session 2 Chair: Eiko Kangsep, <i>Tallinn Technical University, Estonia</i>
16.00-16.20	<b>Continued microelectronics training for the future</b> O. Olesen, <i>Technical University of Denmark, Denmark</i> , I. R. Nielsen, <i>Technoconsult, Denmark</i>
16.20-16.40	<b>An efficient metrics for timing-driven hardware-/software co-synthesis within the DICE design environment</b> T. Hollstein, M. Glesner, <i>Darmstadt Univ. of Technology, Germany</i>
16.40-17.00	<b>An analytical approach to low-power gated-clock finite-state machine solutions</b> A. Liiver, K. Tammemäe, M. Kruus, <i>Tallinn Technical Univ., Estonia</i>
17.00-17.20	<b>RF Power Amplifier Control Curve Approximation</b> M. Koort, <i>Analog Design Ltd., Estonia</i>
17.20-17.40	<b>Low Power SC Structures with Low Threshold MOS Switches</b> V. Männama, R. Sabolotny, <i>Tallinn Technical University, Estonia</i>

14.00-15.40	4. Instrumentation and System Design Session 3 Chair: Toomas Parve, <i>Tallinn Technical University, Estonia</i>
14.00-14.20	<b>A novel fiber-optical angular and linear displacement sensors and the latter as refractometer</b> J. Verkelis, R. Jankevičius, <i>Semiconductor Physics Institute, Lithuania</i> , R. Šarmaitis, <i>Institute of Chemistry, Lithuania</i>
14.20-14.40	<b>Low-Cost High Performance Vector Network Analyser</b> T. Sys, P. Lambrecht, J. Vandewege, <i>Gent University, Belgium</i>
14.40-15.00	<b>Phase-Frequency Detector Modeling for the PLL Applications</b> V. Männama, T. Paavle, <i>Tallinn Technical University, Estonia</i>
15.00-15.20	<b>Portable Measurement Setup for Multi-conductor Transmission Lines</b> T. Sys, B. De Zwaef, A. Everaert, J. Vandewege, <i>Gent Univ. Belgium</i> , S. Temmerman, <i>SEBA service nv, Belgium</i> , P. Boets, L. Van Biesen, <i>Free University of Brussels, Belgium</i>
15.20-15.40	<b>The Class of Test Signals for Dynamic Testing of AD Converters</b> R. Land, <i>Tallinn Technical University, Estonia</i>
15.40-16.00	Coffee
16.00-17.40	10. Education and Training Chair: Toomas Rang, <i>Tallinn Technical University, Estonia</i>
16.00-16.20	<b>Education in Digital and Microprocessor Technology for Students of Applied Informatics</b> H. Krawczyk, J. Sienkiewicz, S. Szejko, <i>Higher Education College, Poland</i>
16.20-16.40	<b>Virtual Laboratory for Research in Dependable Microelectronics</b> K.-H. Diener, G. Elst, <i>FhG EAS IIS, Germany</i> , E. Gramatova, <i>Institute of Informatics, Slovak Republic</i> , W. Kuzmicz, <i>Warsaw University of Technology, Poland</i> , Z. Peng, <i>Linköping University, Sweden</i> , R. Ubar, <i>Tallinn Technical University, Estonia</i>
16.40-17.00	<b>Combining Learning, Training and Research in Laboratory Course for Design and Test</b> R. Ubar, E. Orasson, J. Raik, <i>Tallinn Technical University, Estonia</i> , H.-D. Wuttke, <i>Ilmenau Technical University, Germany</i>
17.00-17.20	<b>Mechatronics in initial vocational training</b> A. Rosin, T. Lehtla, J. Laugis, <i>Tallinn Technical University, Estonia</i>
17.20-17.40	<b>Numerical Calculations of Electrotechnics</b> L. Liivik, R. Jansikene, <i>Tallinn Technical University, Estonia</i>

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**Tuesday 10 Oct. Posters**

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<b>10.00-12.40</b>	<b>3. Integrated Electronics and Chip Design</b>
P-018 4	<b>A 2-V 2-GHz LC-VCO with Large Tuning Range and Low Phase Noise</b> K. Stadius, R. Ahola, V. Porra, <i>Helsinki Univ. of Technology, Finland</i>
P-022 4	<b>The Realization of Artificial Neural Networks in a FPGA</b> J. Dohnal, T. Mainzer, <i>University of West Bohemia, Czech Republic</i>
P-030 4	<b>Impedance Characteristics of SAW Transducers</b> S. Rupkus, M. Adomaitis, <i>Kaunas University of Technology, Lithuania</i>
P-031 4	<b>Researches of intermetallic layers during wave soldering</b> O. Zimarinas, <i>Kaunas University of Technology, Lithuania</i> , L. Soloveytkikas, <i>Joint Stock Company "Rangas", Lithuania</i>
P-037 4	<b>CMOS SP chip for resistor type semiconductor gas sensors</b> A. Kasemaa, T. Rang, <i>Tallinn Technical University, Estonia</i>
P-044 4	<b>A Low-Power Approach to CORDIC Engine Implementation</b> P. Perälä, O. Vainio, <i>Tampere University of Technology, Finland</i> , K. Vihavainen, <i>Nokia Mobile Phones, Finland</i>
P-065 4	<b>A Variable Gate-Pulse-Width Charge Pumping Technique for MOSFET's Characterization</b> H. Bourdoucen, <i>Sultan Qaboos University, Oman U.A.E.</i> , S. Harous, <i>University of Sharjah, United Arab Emirates</i> , F. Rahmoune, <i>University of Boumerdes, Algeria</i>

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**12.40-14.00** Lunch break

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<b>14.00-15.40</b>	<b>6. Telecommunication and Optical Transmission</b>
P-020 4	<b>The use of the WDM technique in optical transport and access networks</b> J. Slovák, M. Vaculik, <i>University of Žilina, Slovak Republic</i>
P-026 4	<b>V-I control of VCOs for multiple band wireless communications</b> E. D. Manolov, <i>Technical University of Sofia, Bulgaria</i> , N. Tchamov, <i>Tampere University of Technology, Finland</i>
P-063 4	<b>DSP-based Voice Signal Compressing System</b> P. Misans, <i>Riga Technical University, Latvia</i> , N. Veselis, J. Lavendelis, G. Linis, <i>University of Latvia, Latvia</i>

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**Tuesday 10 Oct. Posters**

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P-068 4	<b>Signal distortions caused by multipath propagation in DAB transmission</b> A. Meister, E. Lossmann, P. Martverk, <i>Tallinn Technical Univ., Estonia</i>
P-083 4	<b>CATV – video and multimedia system</b> R. Volner, <i>University of Žilina, Slovak Republic</i>
<b>15.40-16.00</b>	Coffee
<b>16.00-17.40</b>	<b>4. Instrumentation and System Design</b>
P-003 4	<b>Practical Experience with Linearity Improvement in Dithered ADC</b> J. Holub, J. Vedral, <i>Czech Technical University, Czech Republic</i>
P-010 4	<b>Investigation of the Properties of the Helical Delay System using Microwave Office 2000 Package</b> V. Daškevičius, J. Skudutis, <i>Vilnius Gediminas Technical University, Lithuania</i>
P-016 4	<b>Design of two-rate decentralized linear quadratic regulators for continuous-time stochastic multivariable control systems</b> I. Astrov, J. Em, E. Rüstern, <i>Tallinn Technical University, Estonia</i>
P-017 4	<b>Design of three-rate continuous-time subcontrollers for the multidimensional stochastic state-space model of an experimental aircraft</b> I. Astrov, A. Pedai, E. Rüstern, <i>Tallinn Technical University, Estonia</i>
P-023 4	<b>Stability monitoring of radiometric standard lamp using solar blind phototube</b> U. Veismann, <i>Tartu Observatory, Estonia</i> , T. Kübarsepp, <i>Helsinki University of Technology, Finland</i>
P-028 4	<b>Estimation of Integrated Security Systems</b> M. Žilys, A. Valinevičius, <i>Kaunas University of Technology, Lithuania</i>
P-035 4	<b>Fault Prediction in Power Network</b> T. Kangilaski, <i>Tallinn Technical University, Estonia</i>
P-057 4	<b>Boundary Scan 1149.4 for MCM test</b> E. Olfati, B. Magnhagen, <i>School of Engineering Jönköping University, Sweden</i>
P-064 4	<b>The MATLAB and SIMULINK-based Software Environment for Development of Mobile Electronic Nose</b> P. Misans, <i>Riga Technical University, Latvia</i> , J. Kleperis, J. Zubkans, V. Eglitis, A. Lusiš, <i>University of Latvia, Latvia</i>

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**Wednesday 11 Oct.**

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**9.00-9.40 Invited talk 4:**

10.00-10.40 **7. Biomedical Electronics**  
Chair: Kalju Meigas, *Tallinn Technical University, Estonia*

10.00-10.20 **CNN based brain electrical activity monitoring system**  
M. Laiho, A. Paasio, K. Halonen, *Helsinki University of Technology, Finland*

10.20-10.40 **Magnetopneumography**  
A. Típek, P. Ripka, P. Kašpar, P. Navrátil, *Czech Technical University, Czech Republic*,  
Dr. Šváblová, *Public Health Center Prague, Czech Republic*

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**10.40-11.00 Coffee**

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11.00-12.20 **1. Electronic Materials and Package Technologies**  
**2. Semiconductor Devices and Simulation**  
Session 2  
Chair: Enn Velmre, *Tallinn Technical University, Estonia*

11.00-11.20 **Feedback amplifier analysis and configurations**  
B. Wilson, *University of Manchester (UMIST), UK*,  
E. M. Cherry, *Monash University, Australia*

11.20-11.40 **Increasing the Speed of Delay Simulation in Digital Circuits**  
R. Ubar, A. Jutman, *Tallinn Technical University, Estonia*

11.40-12.00 **Simulation of Digital Systems with High-Level Decision Diagrams**  
A. Morawiec, *TIMA Laboratory 46, France*,  
J. Raik, R. Ubar, *Tallinn Technical University, Estonia*

12.00-12.20 **Performance Improvement of Memory Cells Based on Reverse Base Current Effect**  
A. N. Bubennikov, A. Zykov, *Moscow Institute of Physics and Technology, Russia*

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**12.40-14.00 Lunch break**

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**Wednesday 11 Oct.**

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10.00-10.40 **8. Power Electronics**  
Session 1  
Chair: Jüri Joller, *Tallinn Technical University, Estonia*

10.00-10.20 **Pulse Width Modulation for Three-Phase Three-Level Converters**  
H. Grotstollen, *University of Paderborn, Germany*

10.20-10.40 **High power factor single- and three-phase diode rectifiers using parallel re-rectification of the ripple power**  
V. Sarv, T. Sakkos, *Tallinn Technical University, Estonia*

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**10.40-11.00 Coffee**

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11.00-12.00 **8. Power Electronics**  
Session 2  
Chair: Horst Grotstollen, *University of Paderborn, Germany*

11.00-11.20 **Tram traction drives and comparison of their control methods**  
J. Joller, M. Lehtla, T. Lehtla, J. Laugis, *Tallinn Technical University, Estonia*

11.20-11.40 **Commutation Processes in the Diode Bridge of Resonant Converter with Alternating Parallel and Series Resonance**  
K. Janson, J. Järvi, T. Vinnal, *Tallinn Technical University, Estonia*

11.40-12.00 **Power factor adjustment by means of direct frequency converters**  
I. Galkin, L. Ribickis, *Riga Technical University, Latvia*

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**12.40-14.00 Lunch break**

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**Wednesday 11 Oct. Posters**

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<b>10.00-12.40</b>	<b>5. Test, Diagnostics and Fault Tolerance</b>
P-009 4	<b>Neural networks for fault diagnosis of analog nonlinear circuits</b> G. Fedi, S. Manetti, M. C. Piccirilli, <i>University of Florence, Italy</i>
P-025 4	<b>Frequency dependence of fault recognition in ultrasonics inspection of thin walled welded joints</b> P. Houfek, <i>Czech Technical University, Czech Republic</i>
P-038 4	<b>Analog linear circuits diagnosis using fault dictionary approach</b> V. Lantsov, S. Mosin, M. Morozov, <i>Vladimir State University, Russia</i>
P-041 4	<b>Linear Methods for Diagnosis in Nonlinear Systems</b> A. Zhirabok, S. Usoltsev, <i>Far Eastern State Technical Univ., Russia</i>
P-060 4	<b>Factors limiting the accuracy of sampling hysteresisgraph</b> A. Platil, P. Ripka, J. Roztočil, P. Kašpar, <i>Czech Technical University, Czech Republic</i>
P-061 4	<b>Testing and comparing of the high-resolution converters ADS1211 and AD7714</b> A. German, P. Ripka, <i>Czech Technical University, Czech Republic</i>
P-087 4	<b>Mapping of VHDL Structures for Generic EDA Database Format IRSYD</b> E. Ivask, P. Ellervee, <i>Tallinn Technical University, Estonia</i>
P-095 4	<b>Preliminary approach to the timing measurements for reverse recovery applied to the SiC Schottky diode model</b> M. Pikkov, <i>Tallinn Technical University, Estonia</i>
P-097 4	<b>Advanced method for ADC testing generators verification</b> J. Roztočil, J. Brossmann, V. Haasz, <i>Czech Technical University, Czech Republic</i>
<b>12.40-14.00</b>	<b>Lunch break</b>
<b>14.00-15.40</b>	<b>1. Electronic Materials and Package Technologies</b>
P-055 4	<b>Separation of the Intrinsic Defects in a Semiconductor by Laser Radiation</b> A. Medvids, <i>Riga Technical University, Latvia</i> , L. Fedorenko, <i>Institute of Semiconductor Physics, Ukraine</i>

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**Wednesday 11 Oct. Posters**

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<b>14.00-15.40</b>	<b>2. Semiconductor Devices and Simulation</b>
P-054 4	<b>The avalanche and secondary breakdown of silicon diodes</b> T. Puritis, <i>Riga Technical University, Latvia</i>
P-077 4	<b>Analysis of peak current for current crowding effect in 4H- and 6H-SiC Schottky structures</b> R. Kurel, T. Rang, <i>Tallinn Technical University, Estonia</i>
P-098 4	<b>LPE structures for power GaAs rectifiers</b> V. Voitovich, <i>AS Clifton, Estonia</i>
<b>14.00-15.40</b>	<b>7. Biomedical Electronic</b>
P-002 4	<b>Susceptance Measurement as a Method for Monitoring the Population of Brewer's Yeast</b> Y. Wang, <i>Shandong College of Light Industry, P. R. of China</i> , K. S. Christensen, <i>Technical University of Denmark, Denmark</i> , H. Hoffmeister, <i>Carlsberg Technological Development, Denmark</i>
P-070 4	<b>The neural network classification algorithms with applications to averaged EEG data</b> A. Raja, E. Lossmann, A. Meister, <i>Tallinn Technical University, Estonia</i> , V. Tuulik, <i>Estonian Institute of Experimental and Clinical Medicine, Estonia</i>
<b>14.00-15.40</b>	<b>8. Power Electronics</b>
P-073 4	<b>Verification of Correctness of Nonlinear Equations in CFD</b> J. O. Meilus, S. Gelzinis, <i>Kaunas University of Technology, Lithuania</i>
P-080 4	<b>Negative Resistance Structures: Optimization of Power Balance</b> R. Taniloo, <i>Tallinn Technical University, Estonia</i>
P-093 4	<b>Analysis of drawbacks and reconstruction problems of the R2-type train</b> J. Laugis, D. Vinnikov, V. Boiko, <i>Tallinn Technical University, Estonia</i>
P-100 4	<b>Effect of the Motor Line Reactor on Bearing Currents in Drives Fed from VSI with PWM</b> A. Kempinski, R. Smolenski, R. Strzelecki, <i>Technical University Zielona Góra, Poland</i> , M. Klytta, <i>University of Applied Sciences Giessen-Friedberg, Germany</i>

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One-day action on October 11, 2000 at 10.00, 11.00, and 14.00:

**"Design and Test of Electronic Circuits and Systems"** in parallel with Baltic Electronics Conference will take place at Tallinn Technical University.

The Forum is sponsored by European JEP VILAB under COPERNICUS program. It is intended to bring together academic expertise and experience of industry in the field of Design and Test of Electronic Circuits and Systems to present their results and discuss new trends and needs in Europe and in Estonia, particularly.

Presentations on the following topics will be carried out:

- new technologies and design tools in electronics,
- overviews about SMEs in different European countries,
- international cooperation in joint European projects,
- presentations from Estonian companies and SMEs.

A panel will be organized on the problems and possibilities of cooperation between universities and industry.

Informal proceedings of presentations will be issued for participants of the User Forum.

The deadline for registration and for submitting contributions (texts, copies of transparencies) is **September 15**.

For registration please send the following data to [raiub@pld.ttu.ee](mailto:raiub@pld.ttu.ee):

1. Name of the participant:
2. Institution:
3. Address (e-mail, phone):
4. Arrival:
5. Departure:
6. Are you registered for BEC: YES/NO
7. Are you intending to make a presentation: TITLE/NO
8. Hotel needed (for those who are not registered for BEC): YES/NO

NB! If you have not registered for BEC and need a hotel, please register at [raiub@pld.ttu.ee](mailto:raiub@pld.ttu.ee) as soon as possible.

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